Starting soon

MEEP
MareNostrum Experimental Exascale Platform

Full Stack Exascale Design

- HPC/DA Applications
- HPC/DA Runtimes
- Virtualization
- OS
- FPGA Emulated HW

Large-scale Emulation for Exascale Systems

- Python
- C/C++
- Java
- OpenMP
- OmpSs-2

FPGA Shell:
- External Interfaces: I/O, DRAM, etc.

Emulation Region:
- Cores: Scalar, vector, etc.
- Accelerators: Tensor, NN, etc.
- Caches and other memory structures