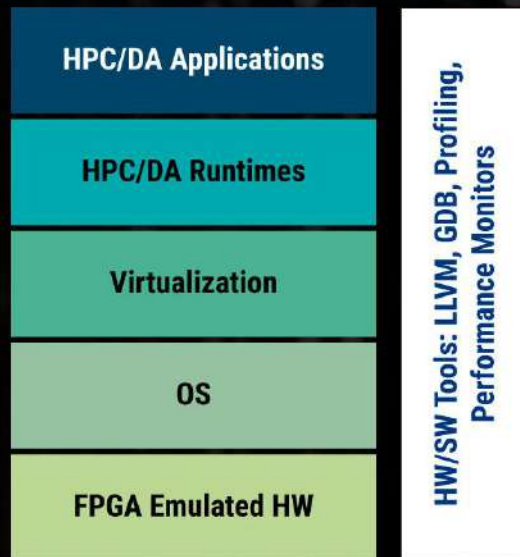


Starting soon

# MEEP

MareNostrum Experimental  
Exascale Platform

## Full Stack Exascale Design



## Large-scale Emulation for Exascale Systems



### FPGA Shell:

- External Interfaces: I/O, DRAM, etc.

### Emulation Region:

- Cores: Scalar, vector, etc.
- Accelerators: Tensor, NN, etc.
- Caches and other memory structures

