



MEEP

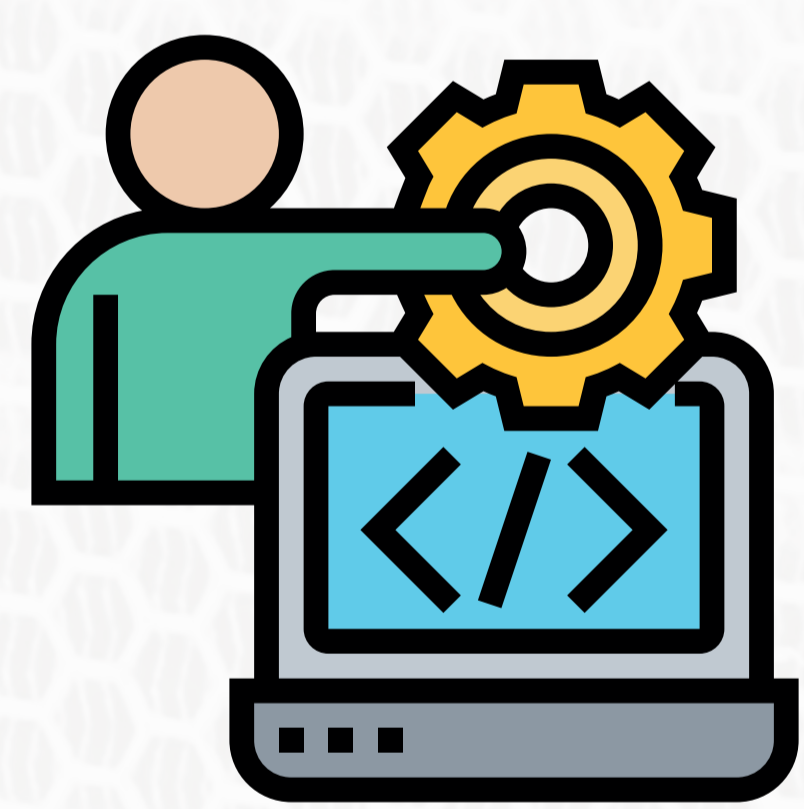
MareNostrum Experimental
Exascale Platform

A flexible FPGA-based emulation platform that will enable hardware/software co-design to explore exascale supercomputer & other hardware designs, based on European-developed IP.

MEEP will provide two important functions:



An evaluation platform of pre-silicon IP and ideas, at speed & scale



A software development tool & experimentation platform that enables software readiness for new hardware

MEEP enables software development by accelerating software maturity, without the limitations of software simulations. Moreover, IP can be tested and validated before moving to silicon, saving time and money.

Partners



www.meep-project.eu



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